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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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Sheet 1 of 1

**Complete if Known**

Application Number	10/057,694
Filing Date	January 23, 2002
First Named Inventor	Chatterjee, Chanchal
Art Unit	<del>Unassigned</del> 2193
Examiner Name	<del>Unassigned</del> Chat C. Do
Attorney Docket Number	018926-008800US

**U.S. PATENT DOCUMENTS**

Examiner	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number Kind Code <sup>2</sup> (If known)			
CD	AA	US-5,903,313	05/11/1999	Tucker et al.	

**OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
CD	AB	"IA-32 Intel® Architecture Software Developer's Manual, Vol. 1: Basic Architecture," Intel Corporation, P.O. Box 7641, Mt. Prospect IL 60056-7641, pp. iii to xii, 10-1 to 10-20, 11-1 to 11-36, (2002)	
CD	AC	"IA-32 Intel® Architecture Software Developer's Manual, Vol. 2: Instruction Set Reference," Intel Corporation, P.O. Box 7641, Mt. Prospect IL 60056-7641, pp. iii to xiii, 3-537 to 3-543, 3-545 to 3-547, 3-598 to 3-599, 3-625 to 3-633, 3-639 to 3-641, 3-657 to 3-658, (2002)	
CD	AD	"Information technology - Coding of moving pictures and associated audio for digital storage media at up to about 1.5 Mbit/s - Part 2: Video," ISO/IEC Copyright Office, Case Postale 56, CH1211 Genève 20, Switzerland, pp. 1-17, 51-57, 77-105, (1993)	
CD	AE	"VR5432 64-Bit MIPS® RISC Microprocessor," Vol. 2, NEC Electronics Inc., U.S.A., pp. iii-xiii, 677-683, (2000)	
CD	AF	"Using Streaming SIMD Extensions 2 (SSE2) in Motion Compensation for Video Decoding and Encoding Version 2.0," AP-942 Using SSE2 in Motion Compensation for Video Decoding and Encoding, Intel Corporation, P.O. Box 7641, Mt. Prospect IL 60056-7641, pp. 1-23, A-1 to A-3, (2000)	
CD	AG	Web page, "Applications Tuning for Streaming SIMD Extensions (continued)," at URL= <a href="http://developer.intel.com/technology/it/q21999/articles/art_5j.htm">http://developer.intel.com/technology/it/q21999/articles/art_5j.htm</a> , printed 6/21/01	
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Examiner Signature	/Chat Do/ (07/13/2006)	Date Considered	07/13/2006
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<sup>1</sup> EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>2</sup> Applicant's unique citation designation number (optional). <sup>3</sup> Applicant is to place a check mark here if English language Translation is attached.

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